

# IST Report

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MIT

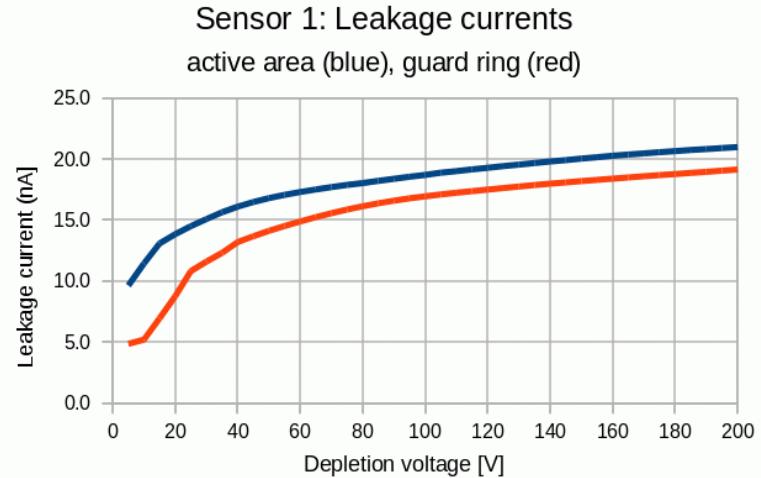
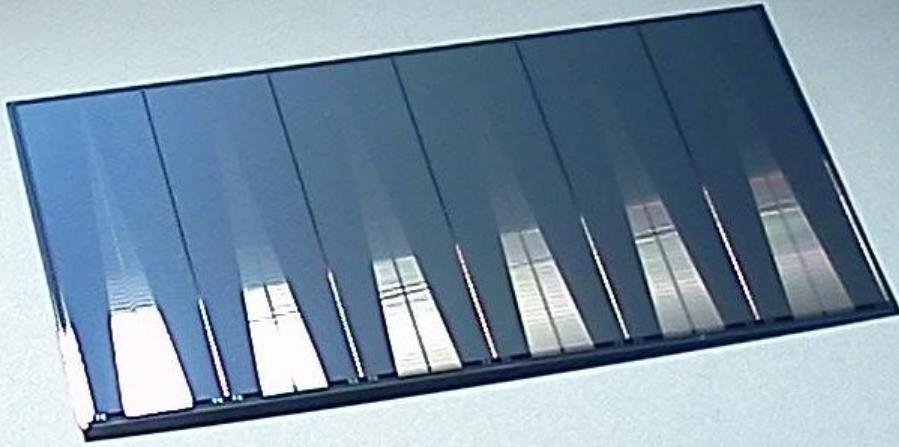
# IST presentation overview

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- IST prototype staves status
  - Critical items/tasks for run-13 installation
  - Critical items/tasks for run-14 installation
  - Needs for CF shop
  - Revisit Risk list
  - Major upcoming procurements
  - Survey plans
  - Upcoming internal Reviews
  - IST cooling liquid
  - Ladder assembly plan
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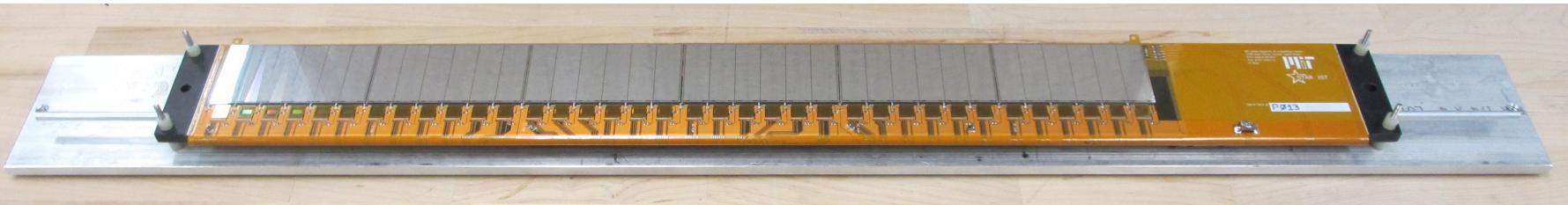
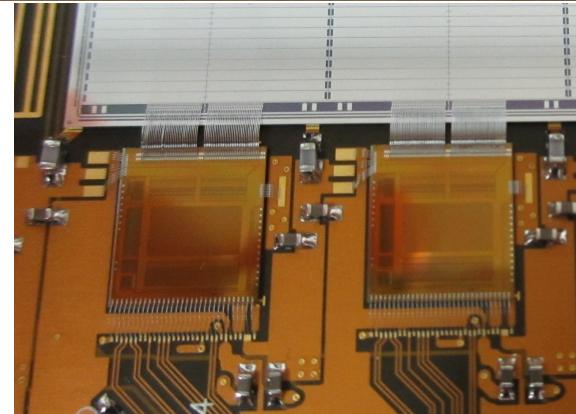
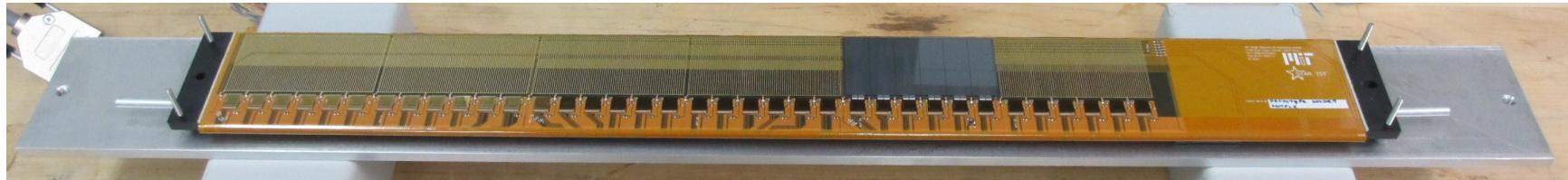
# IST silicon pad sensor



Serial No.	1	2
NG strips	0 %	0 %
coupling short	0	0
readout short	0	0
readout open	0	0
implant short	0	0
implant open	0	0
Bad connection	0	0

**8 prototypes received on Dec. 14<sup>th</sup> 2012  
Pre-tested, excellent quality, will use as is.**

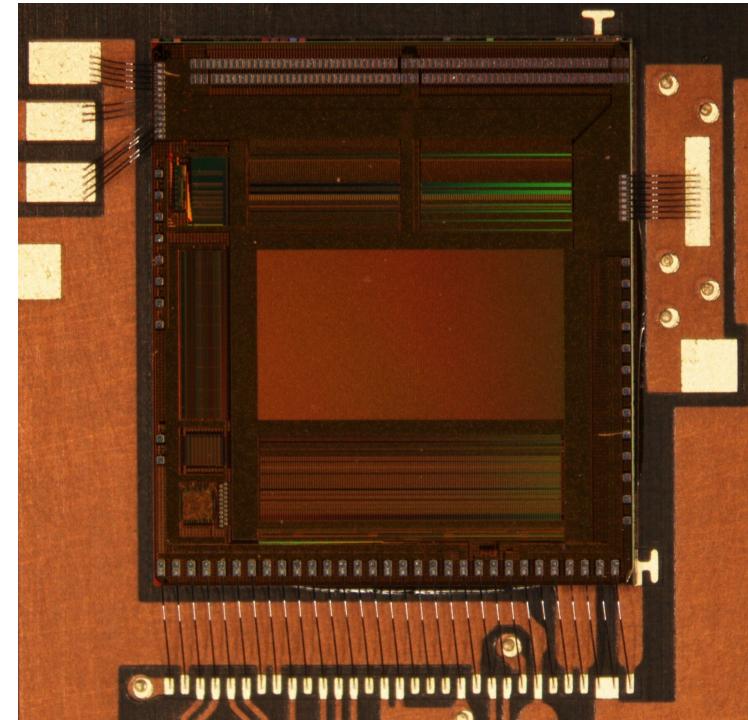
# IST prototype staves



**ProtoStave02, 36 APV's, 1 bonded sensor**

**ProtoStave03, 36 APV's, 6 unbonded sensors**

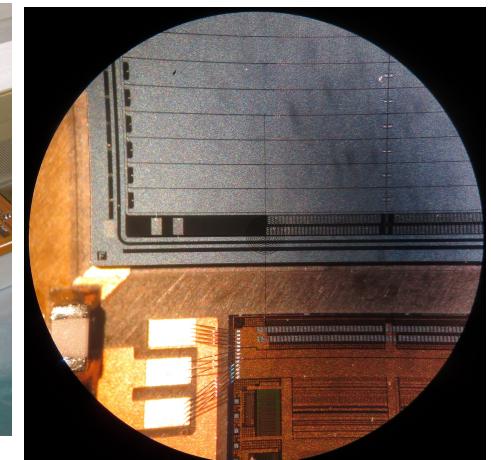
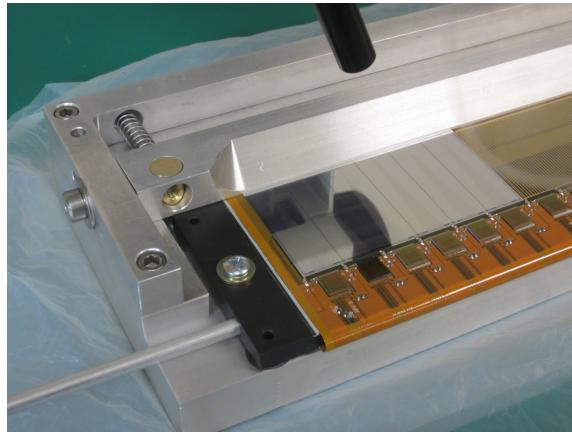
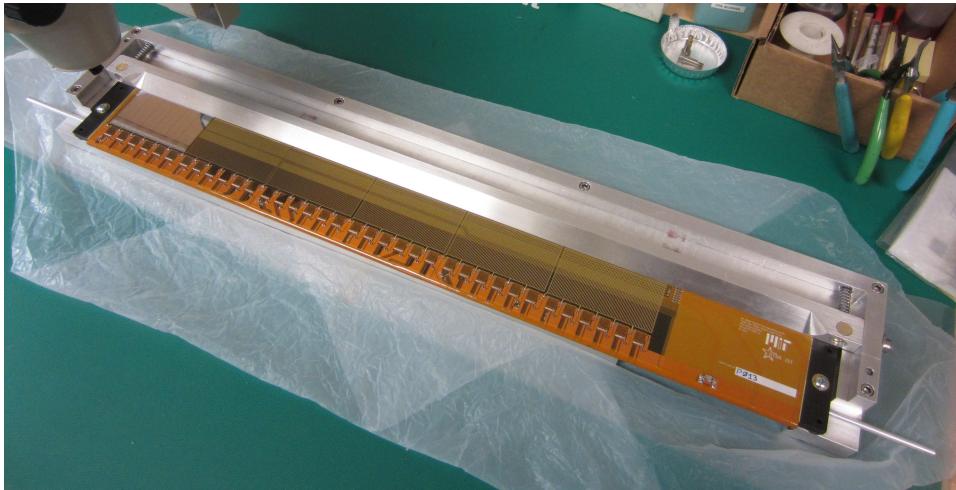
# IST prototype stave bonding



Bonded APV25-S1 chip

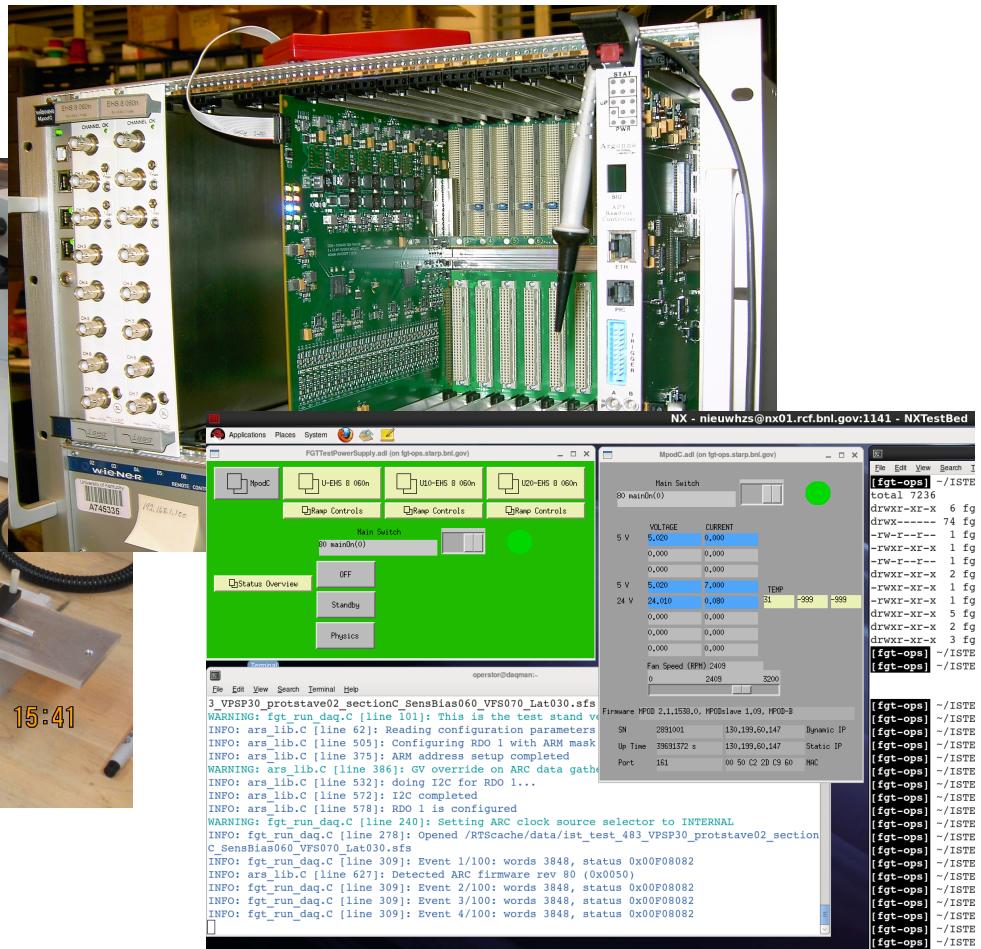
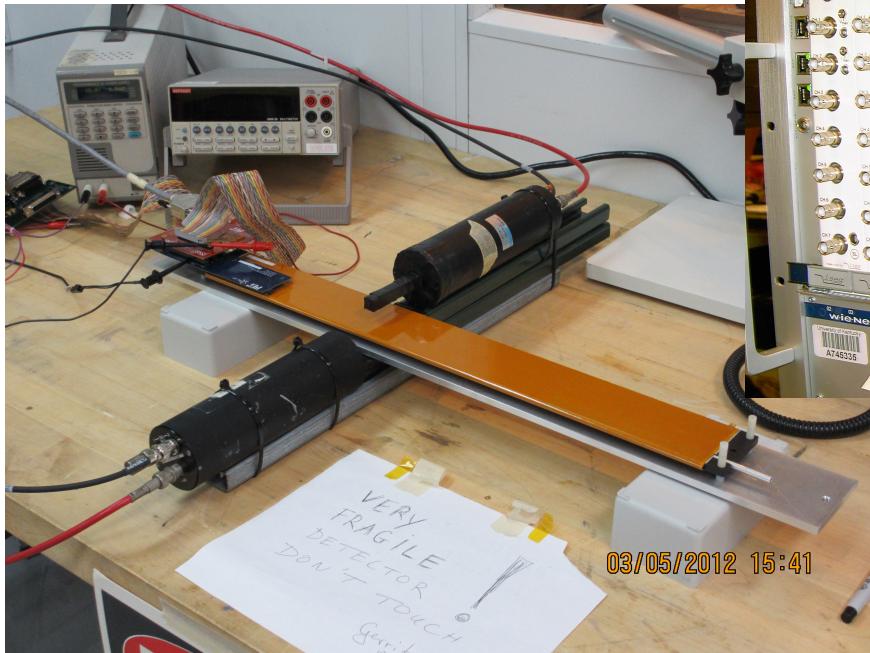
**Bonding of APV's and sensors successful.  
Bonding on hybrid and CF stave works great.  
Reckon with  $\frac{1}{2}$  day for APV's, 1 day for sensors**

# IST prototype stave assembly



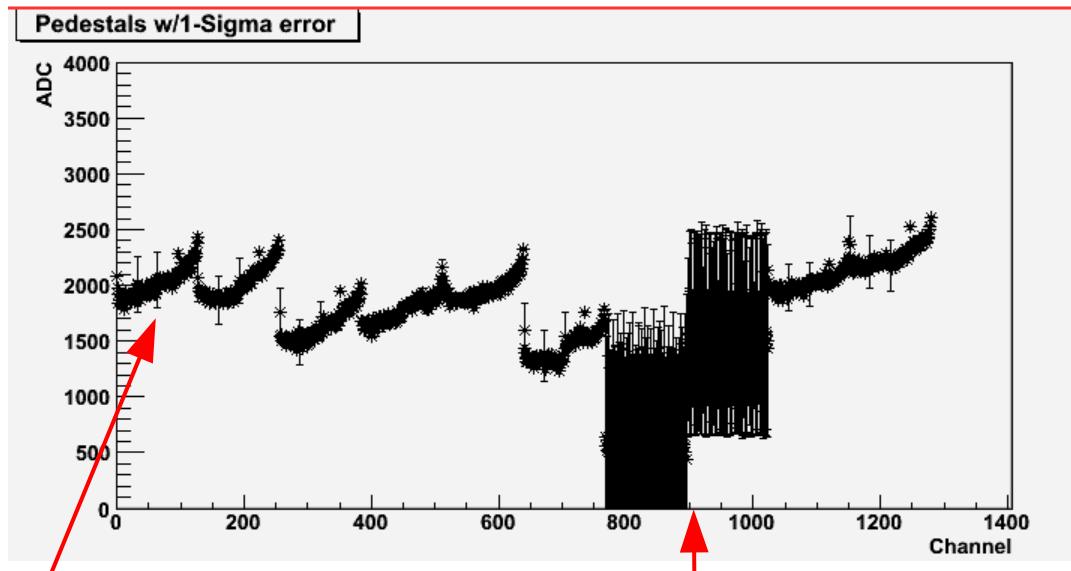
**Placement:  $\frac{1}{2}$  day for APV's,  $\frac{1}{2}$  day for sensors**

# IST prototype stave testing



## Remote controlled cosmic ray testing station

# IST prototype stave issues



I2C?

**Missing termination**

**Not optimal clock termination**

**Adding termination on card solves first problem**

# IST clock termination issue

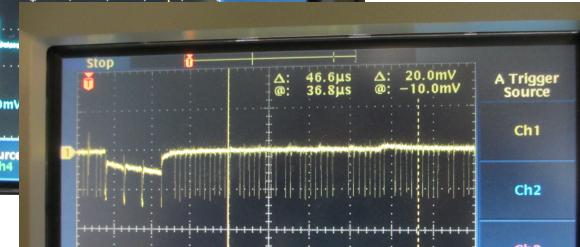
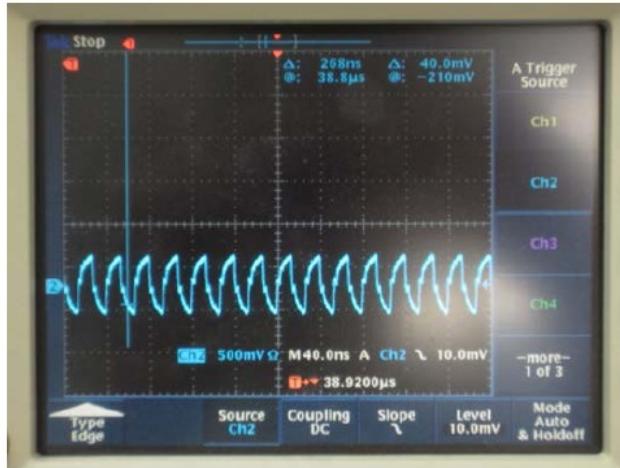
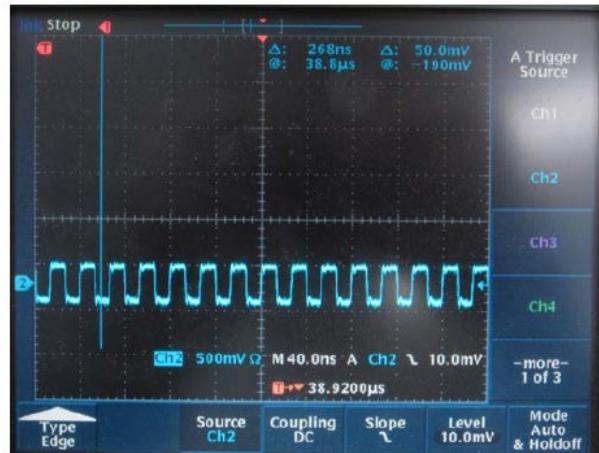
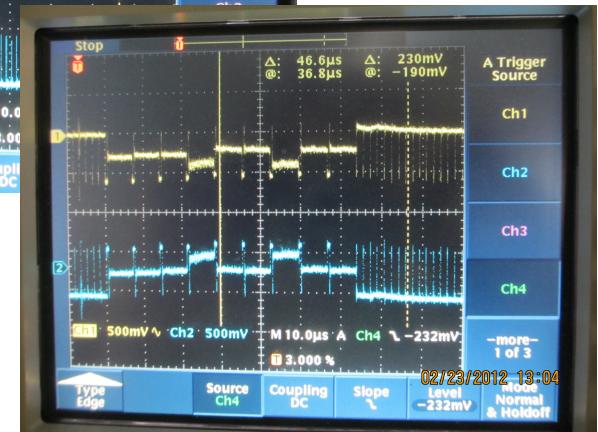


Figure 5: Clock line on APV35 on IST-01 with the design clock termination



**Solved by proper termination**

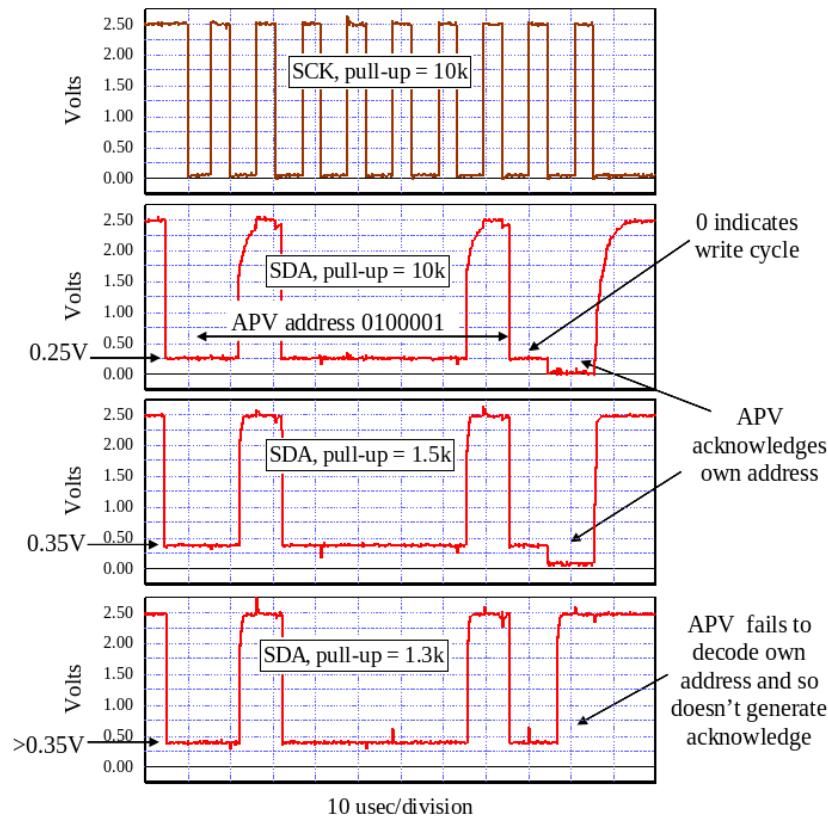
Figure 6: Clock line on APV35 of IST-01 with 50 ohm clock termination



# IST(?) I2C level issue



I2C measurements on the IC APV test setup



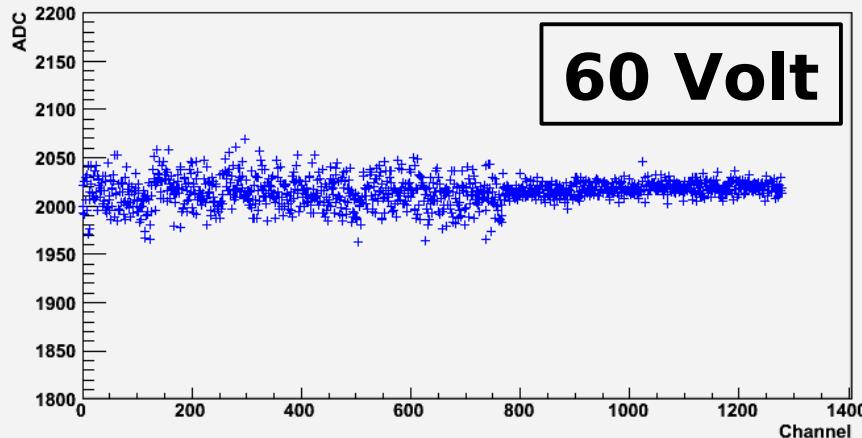
I2C transaction fails on this setup if SDA line not pulled lower than ~ 0.35 Volts

**Problem for FGT! Problem for IST? Solvable!**

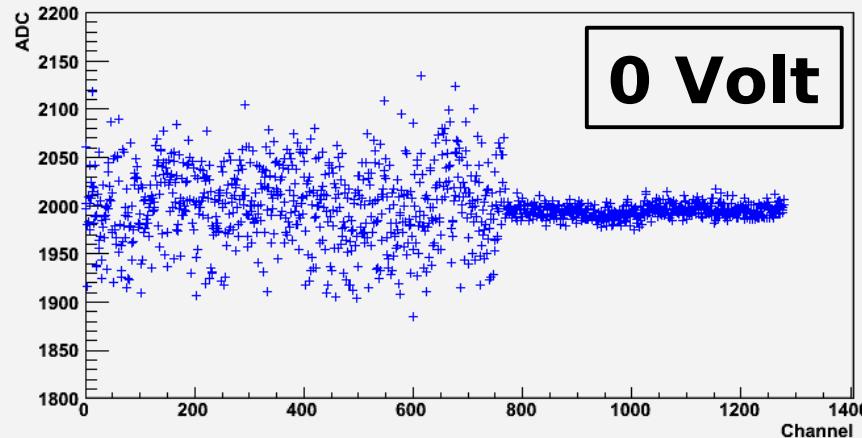
# IST silicon biasing



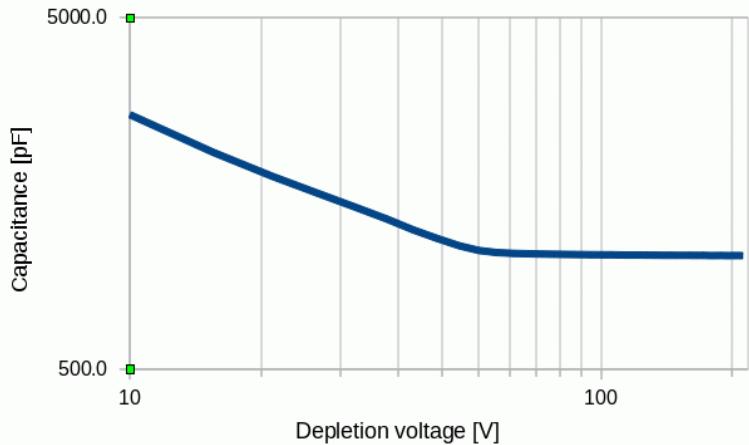
ADC versus Channel



ADC versus Channel

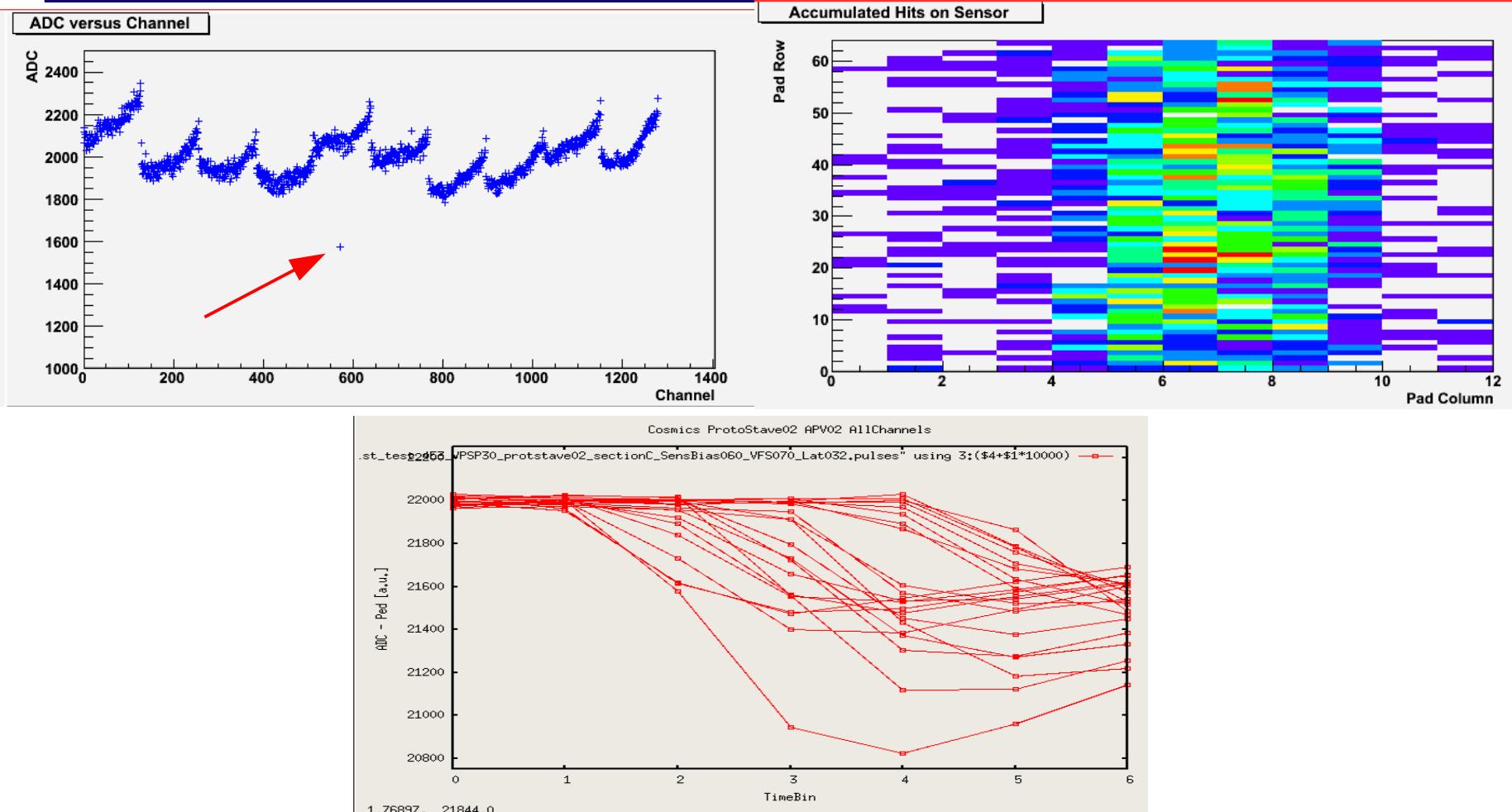


Sensor 1: Full depletion voltage



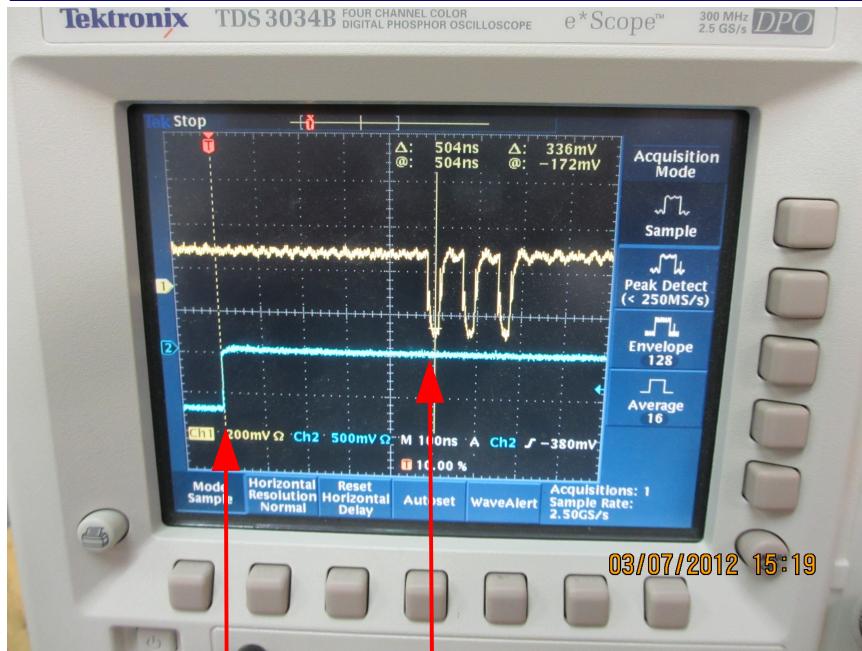
**Noise behaves as expected when applying bias**

# IST cosmic hits and pulses

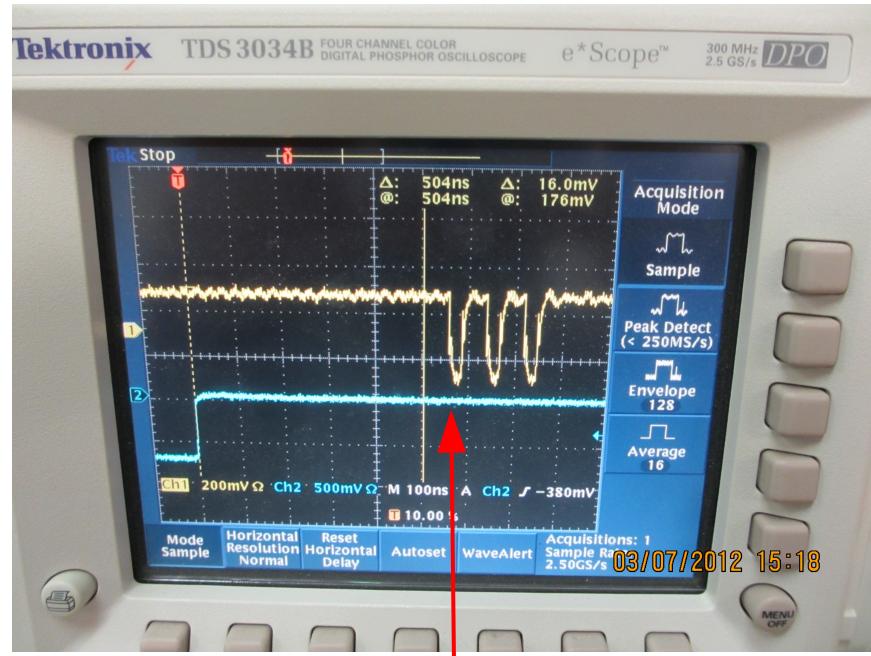


See nice pulses, but see about 4 time bin jitter

# IST slow controls



Readout trigger

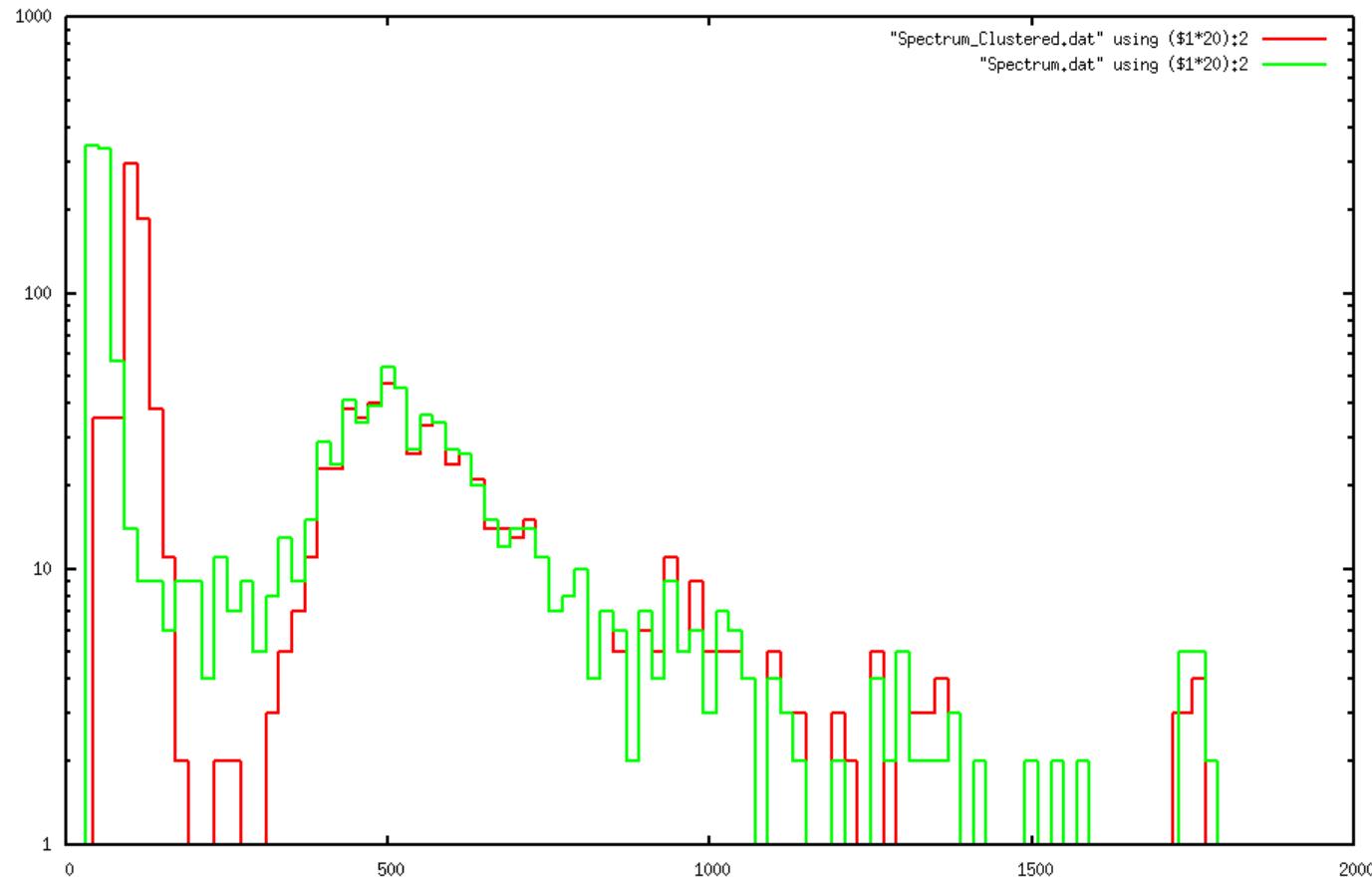


> 100ns later

Scintillator trigger

Random trigger latched by 107ns RHICstrobe!  
'Fix' in software, but need more time bins.....

# IST cosmics spectrum



**Nice spectrum, S/N better than 10:1, 17:1(?)  
Clustering 'split' tracks shows expected dip**

# IST installation

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**Run-13: IST prototype support structures  
on MSC**

**Run-14: ??? Dunno, have the whole thing  
ready ???**

# IST major procurements

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- **MPOD crate, Bias supply, DAQ computer, D-RORC, SIU** ---> in process
- **Silicon sensors**
  - > sign off on prototype staves first
  - Hopefully in April
  - 6 month lead time
- **Hybrids**
  - > sign off on prototype staves first
  - Hopefully in April
  - 1-2 months lead time

# IST cooling system

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- Novec 7200 (**C<sub>4</sub>F<sub>9</sub>OC<sub>2</sub>H<sub>5</sub>**) is a stable cooling and cleaning liquid
  - Opposition in STR against freon, but Novec is not a freon
  - Have to buy cooling tube stock now to avoid delays, can't wait months for STAR to decide
  - Ongoing discussions but bit unclear on how to proceed with current manpower
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# IST stave assembly

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- April-May: CF stave prep  
Hybrid procurement
- July: start stave production  
8-12 staves per week = 1-2 months
- August: 10 staves per week components  
10 staves per week APV's  
10 staves per week bonding  
10 staves per week testing
- October: 10 staves per week sensors  
5 staves per week bonding  
5 staves per week testing (no cosmics!)

Aim is to be ready with 24+3 staves by end of CY12

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